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PATENT APPLICATION

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In the Application of

Attn: OIPE

Nobuaki HASHIMOTO

Application No.: 09/856,924

Docket No.: 109681

Filed: May 30, 2001

For: SEMICONDUCTOR DEVICE AND METHOD OF FABRICATING THE SAME,
CIRCUIT BOARD, AND ELECTRONIC INSTRUMENT

REQUEST FOR CORRECTION OF PALM RECORDS

Director of the U.S. Patent and Trademark Office
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Sir:

Attached is a photocopy of the original filing receipt on which errors have been corrected in red. These errors are being brought to the attention of the Patent and Trademark Office so that it may correct its records.

Respectfully submitted,

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JAO:TJP/slr

Date: August 16, 2001

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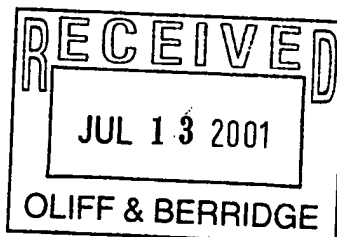
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APPLICATION NUMBER	FILING DATE	GRP ART UNIT	FIL FEE REC'D	ATTY. DOCKET NO.	DRAWINGS	TOT CLAIMS	IND CLAIMS
09/856,924	05/30/2001	2812	1076	109681	10	32	2

CONFIRMATION NO. 6373

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FILING RECEIPT



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Applicant(s)

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Assignment for Published Patent Application
 Seiko Epson Corporation,
 Tokyo, JAPAN;

Domestic Priority data as claimed by applicant

THIS APPLICATION IS A 371 OF PCT/JP00/06767 09/29/2000

Foreign Applications

JAPAN 11-282474(P) 10/04/1999

Projected Publication Date: N/A

Non-Publication Request: No

Early Publication Request: No

Title

Semiconductor device and method of fabricating the same, circuit board, and electronic instrument
 Semiconductor device, method of manufacture thereof, circuit board, and electronic device

Preliminary Class

438

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